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XA-9594
PATENT APPLICATION

#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

MATSUOKA, Hideyuki et al.

Intl. Appln. No.: PCT/JP00/03723

Intl. Filing Date: 8 June 2000

U.S. Appln. No.: 10/009,826

For: SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD
THEREOF

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any
assertion as to materiality or prior art effect, the
documents listed on the attached Form PTO-1449 are hereby
cited.

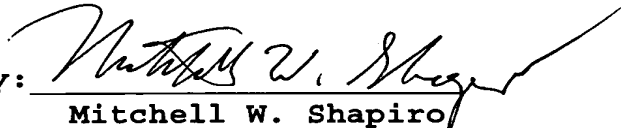
Documents AA and AB on the attached list are cited in
the specification, on page 3, and their relevance is
indicated therein. Documents AC, AD, AL and AM were cited
in the International Search Report (copy attached).

Respectfully submitted,

MWS:sjk

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March 19, 2002

By:


Mitchell W. Shapiro
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FORM PTO-1449				Atty. Docket No. XA-9594		Appln. No. 10/009,826	
LIST OF DOCUMENTS CITED BY APPLICANT				Applicant MATSUOKA, Hideyuki et al.			
				Filing Date		Group	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA	5,745,407	04/28/98	Levy et al.	365	159	
	AB	5,535,156	07/09/96	Levy et al.	365	175	
	AC	5,483,482	01/09/96	Yamada et al.	365	175	
	AD	5,665,978	09/09/97	Uenoyama et al.	257	30	
	AE	5,684,737	11/04/97	Wang et al.	365	175	
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AL	10-125939	05/15/98	Japan			No
	AM	11-17184	01/22/99	Japan			No
	AN						
	AO						
	AP						
	AQ						
OTHER (including author, title, date, pertinent pages, etc.)							
	AR						
	AS						
	AT						
Examiner				Date Considered			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							